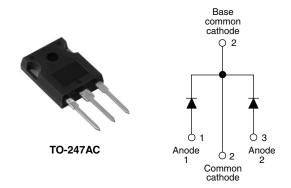


Vishay High Power Products

Schottky Rectifier, 2 x 25 A



PRODUCT SUMMARY			
I _{F(AV)}	2 x 25 A		
V_{R}	30 V		

FEATURES

- 150 °C T_J operation
- Center tap TO-247 package
- Low forward voltage drop
- · High frequency operation



ROHS*

- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 52CPQ030PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	50	А	
V_{RRM}		30	V	
I _{FSM}	t _p = 5 μs sine	2180	А	
V _F	25 Apk, T _J = 125 °C (per leg)	0.38	V	
T _J	Range	- 55 to 150	°C	

VOLTAGE RATINGS				
PARAMETER	SYMBOL	52CPQ030PbF	UNITS	
Maximum DC reverse voltage	V_{R}	30	V	
Maximum working peak reverse voltage	V_{RWM}			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average per leg		50 % duty cycle at T _C = 132 °C, rectangular waveform		25	
See fig. 5 per device	I _{F(AV)}			50	Α
Maximum peak one cycle non-repetitive surge current per leg See fig. 7	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	2180	A
		10 ms sine or 6 ms rect. pulse		600	
Non-repetitive avalanche energy per leg	E _{AS}	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 6 \text{A}, L = 1.5 \text{mH}$		27	mJ
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		6	А

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

52CPQ030PbF

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	25 A	T _J = 25 °C	0.48	V
		50 A		0.55	
		25 A	T _J = 125 °C	0.38	
		50 A		0.49	
Maximum reverse leakage current per leg	g , (1)	T _J = 25 °C	V _R = Rated V _R	1.9	mA
See fig. 2	I _{RM} ⁽¹⁾	T _J = 125 °C		450	
Threshold voltage	$V_{F(TO)}$	T _J = T _J maximum		0.24	V
Forward slope resistance	r _t			5.05	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		4600	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body 7.5		7.5	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10		10000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	Э	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		D	DC operation See fig. 4	0.8	0.8	
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	0.4	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25		
A consistence to a sector to				6	g	
Approximate weight			0.21	OZ.		
Mounting torque ——	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf \cdot in)	
Marking device Case style TO-247AC (JEDEC)		52CP	Q030			

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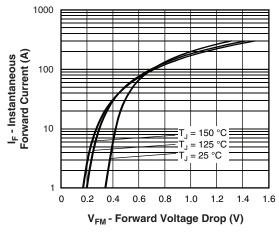


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

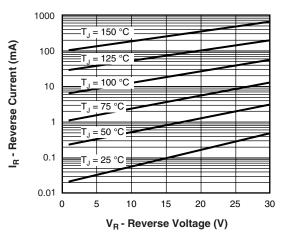


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

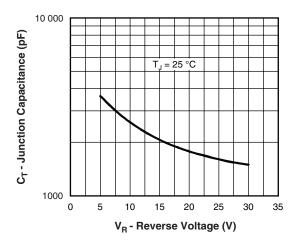


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

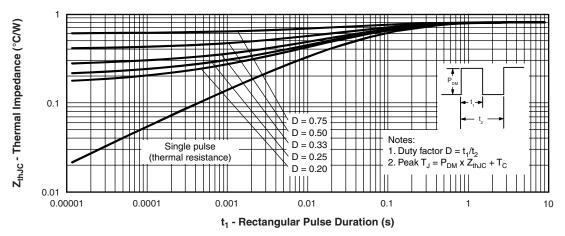


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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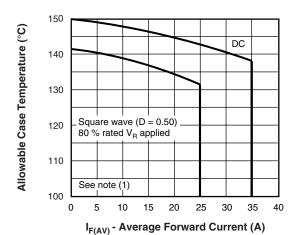


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

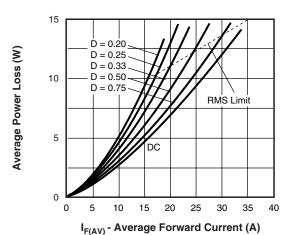
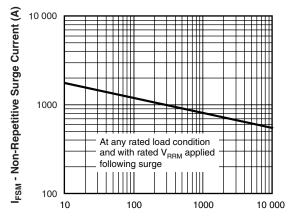


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

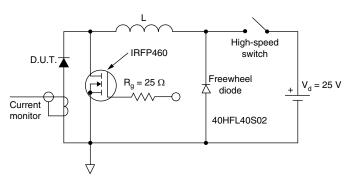


Fig. 8 - Unclamped Inductive Test Circuit

Note

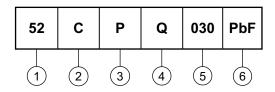
 $^{(1)}$ Formula used: T $_{C}$ = T $_{J}$ - (Pd + Pd $_{REV}$) x R $_{th,JC}$; Pd = Forward power loss = I $_{F(AV)}$ x V $_{FM}$ at (I $_{F(AV)}$ /D) (see fig. 6); Pd $_{REV}$ = Inverse power loss = V $_{R1}$ x I $_{R}$ (1 - D); I $_{R}$ at V $_{R1}$ = 80 % rated V $_{R}$



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ORDERING INFORMATION TABLE

Device code



1 - Current rating (50 A)

2 - Circuit configuration:

C = Common cathode

3 - Package:

P = TO-247

4 - Schottky "Q" series

5 - Voltage code (030 = 30 V)

6 - None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95223		
Part marking information	http://www.vishay.com/doc?95226		

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Vishay

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